

```

1      PAGE      118,121
2      TITLE DISK2 ---- 10/28/85 FIXED DISK BIOS
3
4      ;-- INT 13 -----
5      ;
6      ; FIXED DISK I/O INTERFACE
7      ;
8      ; THIS INTERFACE PROVIDES ACCESS TO FIXED DISKS
9      ; THROUGH THE IBM FIXED DISK CONTROLLER.
10     ;
11     ;-----
12
13     ;-----
14     ; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
15     ; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
16     ; THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS,
17     ; NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE
18     ; ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT
19     ; VIOLATE THE STRUCTURE AND DESIGN OF BIOS.
20     ;-----
21
22     ; INPUT (AH = HEX VALUE)
23
24     ; (AH) = 00H RESET DISK (DL = 80H,81H) / DISKETTE
25     ; (AH) = 01H READ THE STATUS OF THE LAST DISK OPERATION INTO (AL)
26     ; NOTE: DL < 80H - DISKETTE
27     ; DL > 80H - DISK
28     ; (AH) = 02H READ THE DESIRED SECTORS INTO MEMORY
29     ; (AH) = 03H WRITE THE DESIRED SECTORS FROM MEMORY
30     ; (AH) = 04H VERIFY THE DESIRED SECTORS
31     ; (AH) = 05H FORMAT THE DESIRED TRACK
32     ; (AH) = 06H FORMAT THE DESIRED TRACK AND SET BAD SECTOR FLAGS
33     ; (AH) = 07H FORMAT THE DRIVE STARTING AT THE DESIRED TRACK
34     ; (AH) = 08H RETURN THE CURRENT DRIVE PARAMETERS
35
36     ; (AH) = 09H INITIALIZE DRIVE PAIR CHARACTERISTICS
37     ; INTERRUPT 41H POINTS TO DATA BLOCK
38     ; (AH) = 0AH READ LONG
39     ; (AH) = 0BH WRITE LONG
40     ; NOTE: READ AND WRITE LONG ENCOMPASS
41     ; 512 BYTES + 4 BYTES OF ECC
42     ; (AH) = 0CH SEEK
43     ; (AH) = 0DH ALTERNATE DISK RESET (SEE DL)
44     ; (AH) = 0EH READ SECTOR BUFFER
45     ; (AH) = 0FH WRITE SECTOR BUFFER,
46     ; (RECOMMENDED PRACTICE BEFORE FORMATTING)
47     ; (AH) = 10H TEST DRIVE READY
48     ; (AH) = 11H RECALIBRATE
49     ; (AH) = 12H CONTROLLER RAM DIAGNOSTIC
50     ; (AH) = 13H DRIVE DIAGNOSTIC
51     ; (AH) = 14H CONTROLLER INTERNAL DIAGNOSTIC
52
53     ;
54     ; REGISTERS USED FOR FIXED DISK OPERATIONS
55
56     ; (DL) - DRIVE NUMBER (80H-87H FOR DISK, VALUE CHECKED)
57     ; (DH) - HEAD NUMBER (0-7D ALLOWED, NOT VALUE CHECKED)
58     ; (CH) - CYLINDER NUMBER (0-1023D, NOT VALUE CHECKED) (SEE CL)
59     ; (CL) - SECTOR NUMBER (1-17D, NOT VALUE CHECKED)
60
61     ; NOTE: HIGH 2 BITS OF CYLINDER NUMBER ARE PLACED
62     ; IN THE HIGH 2 BITS OF THE CL REGISTER
63     ; (10 BITS TOTAL)
64     ; (AL) - NUMBER OF SECTORS (MAXIMUM POSSIBLE RANGE 1-80H,
65     ; FOR READ/WRITE LONG 1-79H)
66     ; (INTERLEAVE VALUE FOR FORMAT 1-16D)
67     ; (ES:BX) - ADDRESS OF BUFFER FOR READS AND WRITES,
68     ; (NOT REQUIRED FOR VERIFY)
69
70     ; OUTPUT
71     ; AH = STATUS OF CURRENT OPERATION
72     ; STATUS BITS ARE DEFINED IN THE EQUATES BELOW
73     ; CY = 0 SUCCESSFUL OPERATION (AH= 00H ON RETURN)
74     ; CY = 1 FAILED OPERATION (AH HAS ERROR REASON)
75
76     ; NOTE: ERROR 11H INDICATES THAT THE DATA READ HAD A RECOVERABLE
77     ; ERROR WHICH WAS CORRECTED BY THE ECC ALGORITHM. THE DATA
78     ; IS PROBABLY GOOD, HOWEVER THE BIOS ROUTINE INDICATES AN
79     ; ERROR TO ALLOW THE CONTROLLING PROGRAM A CHANCE TO DECIDE
80     ; FOR ITSELF. THE ERROR MAY NOT RECUR IF THE DATA IS
81     ; REWRITTEN. (AL) CONTAINS THE BURST LENGTH.
82
83     ; IF DRIVE PARAMETERS WERE REQUESTED,
84
85     ; DL = NUMBER OF CONSECUTIVE ACKNOWLEDGING DRIVES
86     ; ATTACHED (0-2) (CONTROLLER CARD ZERO TALLY ONLY)
87     ; DH = MAXIMUM USEABLE VALUE FOR HEAD NUMBER
88     ; CH = MAXIMUM USEABLE VALUE FOR CYLINDER NUMBER
89     ; CL = MAXIMUM USEABLE VALUE FOR SECTOR NUMBER
90     ; AND CYLINDER NUMBER HIGH BITS
91
92     ; IF AN ERROR OCCURS ON READ DRIVE PARAMETERS,
93
94     ; AH = ERROR CODE (INIT_FAIL)
95     ; AL = CX = DX = 0
96
97     ; REGISTERS WILL BE PRESERVED EXCEPT WHEN THEY ARE USED TO RETURN
98     ; INFORMATION.
99
100    ; NOTE: IF AN ERROR IS REPORTED BY THE DISK CODE, THE APPROPRIATE
101    ; ACTION IS TO RESET THE DISK, THEN RETRY THE OPERATION.
102    ;-----

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103                                     PAGE
104                                     ;-----
105                                     ;      ERROR RETURN STATUS (AH)= ??H WHEN CY= 1      :
106                                     ;-----
107
108 = 00FF          SENSE_FAIL      EQU      OFFH          ; SENSE OPERATION FAILED
109 = 00CC          WRITE_FAULT     EQU      0CCH          ; WRITE FAULT ON SELECTED DRIVE
110 = 00BB          UNDEF_ERR       EQU      0BBH          ; UNDEFINED ERROR OCCURRED
111 = 0080          TIME_OUT        EQU      080H          ; ATTACHMENT FAILED TO RESPOND
112 = 0040          BAD_SEEK        EQU      040H          ; SEEK OPERATION FAILED
113 = 0020          BAD_CNTLRL      EQU      020H          ; CONTROLLER HAS FAILED
114 = 0011          DATA_CORRECTED EQU      011H          ; ECC CORRECTED DATA ERROR
115 = 0010          BAD_ECC         EQU      010H          ; BAD ECC ON DISK READ
116 = 000B          BAD_TRACK       EQU      00BH          ; BAD TRACK FLAG DETECTED
117 = 0009          DMA_BOUNDARY     EQU      009H          ; ATTEMPT TO DMA ACROSS 64K BOUNDARY
118 = 0007          INIT_FAIL       EQU      007H          ; DRIVE PARAMETER ACTIVITY FAILED
119 = 0005          BAD_RESET       EQU      005H          ; RESET FAILED
120 = 0004          RECORD_NOT_FND  EQU      004H          ; REQUESTED SECTOR NOT FOUND
121 = 0002          BAD_ADDR_MARK    EQU      002H          ; ADDRESS MARK NOT FOUND
122 = 0001          BAD_CMD         EQU      001H          ; BAD COMMAND PASSED TO DISK I/O
123
124                                     ;-----
125                                     ;      INTERRUPT AND STATUS AREAS      :
126                                     ;-----
127
128 0000          ABS0      SEGMENT AT 0H
129 0034          ORG      00DH*4          ; FIXED DISK INTERRUPT VECTOR
130 0034          LABEL    DWORD
131 004C          ORG      013H*4          ; DISK INTERRUPT VECTOR
132 004C          LABEL    DWORD
133 0064          ORG      019H*4          ; BOOTSTRAP INTERRUPT VECTOR
134 0064          LABEL    DWORD
135 0078          ORG      01EH*4          ; DISKETTE PARAMETERS
136 0078          LABEL    DWORD
137 0100          ORG      040H*4          ; NEW DISKETTE INTERRUPT VECTOR
138 0100          LABEL    DWORD
139 0104          ORG      041H*4          ; FIXED DISK PARAMETER VECTOR
140 0104          LABEL    DWORD
141 7C00          ORG      7C00H          ; BOOTSTRAP LOADER VECTOR
142 7C00          LABEL    FAR
143 7C00          ABS0      ENDS
144
145 0000          DATA     SEGMENT AT 40H
146 006C          ORG      06CH
147 006C ????     TIMER_LOW  DW      ?          ; TIMER LOW WORD
148 0072          ORG      072H
149 0072 ????     RESET_FLAG DW      ?          ; 1234H IF KEYBOARD RESET UNDERWAY
150 0074          ORG      074H
151 0074 ??       DISK_STATUS DB      ?          ; FIXED DISK STATUS BYTE
152 0075 ??       HF_NUM     DB      ?          ; COUNT OF FIXED DISK DRIVES
153 0076 ??       CONTROL_BYTE DB      ?        ; CONTROL BYTE DRIVE OPTIONS
154 0077 ??       PORT_OFF   DB      ?          ; PORT OFFSET
155 0078          DATA     ENDS
156
157 0000          CODE      SEGMENT
158
159                                     ;-----
160                                     ;      HARDWARE SPECIFIC VALUES      :
161                                     ;-----
162                                     ;      - CONTROLLER I/O PORT          :
163                                     ;      > WHEN READ FROM:              :
164                                     ;      HF_PORT+0 - READ DATA (FROM CONTROLLER TO CPU) :
165                                     ;      HF_PORT+1 - READ CONTROLLER HARDWARE STATUS :
166                                     ;      (CONTROLLER TO CPU)              :
167                                     ;      HF_PORT+2 - READ CONFIGURATION SWITCHES :
168                                     ;      HF_PORT+3 - NOT USED              :
169                                     ;      > WHEN WRITTEN TO:              :
170                                     ;      HF_PORT+0 - WRITE DATA (FROM CPU TO CONTROLLER) :
171                                     ;      HF_PORT+1 - CONTROLLER RESET      :
172                                     ;      HF_PORT+2 - GENERATE CONTROLLER SELECT PULSE :
173                                     ;      HF_PORT+3 - WRITE PATTERN TO DMA AND INTERRUPT :
174                                     ;      MASK REGISTER                    :
175                                     ;-----
176
177
178 =          CMD_BLOCK      EQU      BYTE PTR [BP]-8 ; CMD_BLOCK HEAD
179 = 0320     HF_PORT        EQU      0320H          ; DISK PORT
180 = 0020     INTA00         EQU      020H          ; 8259 PORT
181 = 0021     INTA01         EQU      021H          ; 8259 PORT
182 = 0020     IOI           EQU      020H          ; END OF INTERRUPT COMMAND
183 = 0008     R1_BUSY        EQU      00001000B     ; DISK PORT 1 BUSY BIT
184 = 0004     R1_BUS         EQU      00000100B     ; COMMAND/DATA BIT
185 = 0002     R1_IOMODE      EQU      00000010B     ; MODE BIT
186 = 0001     R1_REQ         EQU      00000001B     ; REQUEST BIT
187
188 = 0047     DMA_READ        EQU      01000111B     ; CHANNEL 3 (047H)
189 = 004B     DMA_WRITE      EQU      01001011B     ; CHANNEL 3 (04BH)
190 = 0000     DMA           EQU      000H          ; DMA ADDRESS
191 = 0082     DMA_HIGH       EQU      082H          ; PORT FOR HIGH 4 BITS OF DMA
192
193 = 0000     TST_RDY_CMD     EQU      00000000B     ; CNTLR READY (00H)
194 = 0001     RECAL_CMD      EQU      00000001B     ; RECAL (01H)
195 = 0003     SENSE_CMD      EQU      00000011B     ; SENSE (03H)
196 = 0004     FMTDRV_CMD     EQU      00000100B     ; DRIVE (04H)
197 = 0005     CHK_TRK_CMD    EQU      00000101B     ; T CHK (05H)
198 = 0006     FMTTRK_CMD     EQU      00000110B     ; TRACK (06H)
199 = 0007     FMTBAD_CMD     EQU      00000111B     ; BAD (07H)
200 = 0008     READ_CMD       EQU      00001000B     ; READ (08H)
201 = 000A     WRITE_CMD      EQU      00001010B     ; WRITE (0AH)
202 = 000B     SEEK_CMD       EQU      00001011B     ; SEEK (0BH)
203 = 000C     INIT_DRV_CMD   EQU      00001100B     ; INIT (0CH)
204 = 000D     RD_ECC_CMD     EQU      00001101B     ; BURST (0DH)
205 = 000E     RD_BUFF_CMD    EQU      00001110B     ; BUFFER (0EH)
206 = 000F     WR_BUFF_CMD    EQU      00001111B     ; BUFFER (0FH)
207 = 0080     RAM_DIAG_CMD   EQU      11100000B     ; RAM (E0H)
208 = 00E3     CHK_DRV_CMD    EQU      11100011B     ; DRV (E3H)
209 = 00E4     CNTLR_DIAG_CMD EQU      11100100B     ; CNTLR (E4H)
210 = 00E5     RD_LONG_CMD    EQU      11100101B     ; RLONG (E5H)
211 = 00E6     WR_LONG_CMD    EQU      11100110B     ; WLONG (E6H)
212
213 = 0008     MAX_FILE        EQU      8
214 = 0002     S_MAX_FILE     EQU      2

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215
216
217 0000
218 0000 55
219 0001 AA
220 0002 08
221
222
223
224
225
226
227
228
229
230
231 0003
232 0003 EB 35
233 0005 35 39 58 37 32 39
234 31 20 28 43 29 20
235 43 4F 50 59 52 49
236 47 48 54 20 49 42
237 4D 20 20 43 4F 52
238 50 2E
239 0025 2C 31 39 38 32 20
240 2C 31 39 38 35 2E
241 0031 20 31 30 2F 32 38
242 2F 38 35
243 003A
244 003A 2B C0
245 003C 8E D8
246 003E FA
247 003F A1 004C R
248 0042 A3 0100 R
249 0045 A1 004E R
250 0048 A3 0102 R
251 004B C7 06 004C R 0251 R
252 0051 8C 0E 004E R
253 0055 B8 0755 R
254 0058 A3 0034 R
255 005B 8C 0E 0036 R
256 005F C7 06 0064 R 0192 R
257 0065 8C 0E 0066 R
258 0069 C7 06 0104 R 03FF R
259 006F 8C 0E 0106 R
260 0073 FB
261
262
263 0074 B8 ---- R
264 0077 8E D8
265 0079 C6 06 0074 R 00
266 007E C6 06 0075 R 00
267 0083 C6 06 0077 R 00
268 0088 B9 0025
269 008B
270 008B E8 0177 R
271 008E 73 05
272 0090 E2 F9
273 0092 E9 0154 R
274 0095
275 0095 B9 0001
276 0098 BA 0080
277 009B B8 1200
278 009E CD 13
279 00A0 73 03
280 00A2 E9 0154 R
281 00A5
282 00A5 B8 1400
283 00A8 CD 13
284 00AA 73 03
285 00AC E9 0154 R
286 00AF
287 00AF C7 06 006C R 0000
288 00B5 81 3E 0072 R 1234
289 00BB 75 06
290 00BD C7 06 006C R 019A
291 00C3
292 00C3 FA
293 00C4 E4 21
294 00C6 24 FE
295 00C8 E6 21
296 00CA FB
297 00CB
298 00CB E8 0177 R
299 00CE 72 07
300 00D0 B8 1000
301 00D3 CD 13
302 00D5 73 0A
303 00D7
304 00D7 A1 006C R
305 00DA 3D 01BE
306 00DD 72 EC
307 00DF EB 73
308 00E1
309 00E1 B8 1100
310 00E4 CD 13
311 00E6 72 6C
312
313 00E8 B8 0900
314 00EB CD 13
315 00ED 72 65
316
317 00EF B8 C800
318 00F2 8E C0
319 00F4 2B DB
320 00F6 B8 0F00
321 00F9 CD 13
322 00FB 72 57
323
324 00FD FE 06 0075 R
325 0101 BA 0213
326 0104 B0 00
327 0106 EE
328 0107 BA 0321

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PAGE

ASSUME CS:CODE,DS:ABS0

ORG 0H

DB 055H ; GENERIC BIOS HEADER

DB 0AAH

DB 08D ; 4K MODULE

; FIXED DISK I/O SETUP :

;

; - ESTABLISH TRANSFER VECTORS FOR THE FIXED DISK :

; - PERFORM POWER ON DIAGNOSTICS :

; SHOULD AN ERROR OCCUR A "1701" MESSAGE IS DISPLAYED :

;

DISK_SETUP PROC FAR

JMP SHORT L3

DB '59X7291 (C) COPYRIGHT IBM CORP.' ; COPYRIGHT NOTICE

DB ',1982 ,1985.'

DB ' 10/28/85' ; RELEASE MARKER

L3:

SUB AX,AX ; ADDRESS LOW RAM

MOV DS,AX

CLI

MOV AX,WORD PTR ORG_VECTOR ; LOAD DISKETTE IP

WORD PTR DISK_VECTOR,AX ; STORE AT INT 40H

AX,WORD PTR ORG_VECTOR+2 ; LOAD DISKETTE CS

WORD PTR DISK_VECTOR+2,AX ; STORE AT INT 40H

WORD PTR ORG_VECTOR,OFFSET DISK_IO ; FIXED DISK HANDLER

WORD PTR ORG_VECTOR+2,CS ; AT INT 13H

AX,OFFSET HD_INT ; FIXED DISK INTERRUPT

WORD PTR HDISK_INT,AX ; HANDLER AT INT 0DH

WORD PTR HDISK_INT+2,CS

WORD PTR BOOT_VEC,OFFSET BOOT_STRAP ; BOOTSTRAP ROUTINE AT

WORD PTR BOOT_VEC+2,CS ; INT 19H

WORD PTR HF_TBL_VEC,OFFSET FD_TBL ; PARAMETER TABLE AT

WORD PTR HF_TBL_VEC+2,CS ; INT 41H

STI

ASSUME DS:DATA

MOV AX,DATA ; ESTABLISH SEGMENT

MOV DS,AX

MOV DISK_STATUS,0 ; RESET THE STATUS INDICATOR

HF_NUM,0 ; ZERO COUNT OF DRIVES

PORT_OFF,0 ; ZERO CARD OFFSET

CX,25H ; RETRY COUNT

L4:

CALL HD_RESET_1 ; RESET CONTROLLER

JNC L7

LOOP L4 ; TRY RESET AGAIN

JMP ERROR_EX

L7:

MOV CX,1

MOV DX,80H

MOV AX,1200H ; CONTROLLER DIAGNOSTICS

INT 13H ; CHECK THE INTERNAL RAM

JNC P7

JMP ERROR_EX ; BUFFERS

P7:

MOV AX,1400H ; CONTROLLER DIAGNOSTICS

INT 13H ; INTERNAL CHECKSUM AND

JNC P9

JMP ERROR_EX ; ECC CIRCUITRY TEST.

P9:

MOV TIMER_LOW,0 ; ZERO TIMER

CMP RESET_FLAG,1234H ; KEYBOARD RESET

JNE P8

MOV TIMER_LOW,410D ; SKIP WAIT ON RESET

P8:

CLI ; DISABLE INTERRUPTS

IN AL,INTA01 ; TIMER

AND AL,0FEH ; ENABLE TIMER

OUT INTA01,AL ; START TIMER

STI ; INTERRUPTS ON

P4:

CALL HD_RESET_1 ; RESET CONTROLLER

JC P10

MOV AX,1000H ; TEST TO SEE IF THE DRIVE

INT 13H ; IS READY

JNC P2

P10:

MOV AX,TIMER_LOW

CMP AX,446D ; 25 SECONDS

JB P4

JMP SHORT ERROR_EX

P2:

MOV AX,1100H ; RECALIBRATE THE DRIVE 0

INT 13H

JC ERROR_EX

MOV AX,0900H ; SET DRIVE PARAMETERS

INT 13H ; FOR DRIVE 0

JC ERROR_EX

MOV AX,0C800H ; DMA TO BUFFER

MOV ES,AX ; SET SEGMENT

SUB BX,BX

MOV AX,0F00H ; WRITE SECTOR BUFFER

INT 13H

JC ERROR_EX

INC HF_NUM ; DRIVE ZERO RESPONDED

MOV DX,213H ; EXPANSION BOX

MOV AL,0

OUT DX,AL ; TURN BOX OFF

MOV DX,321H ; TEST IF CONTROLLER

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329 010A EC          IN      AL,DX          ; ... IS IN THE SYSTEM UNIT
330 010B 24 0F      AND      AL,0FH
331 010D 3C 0F      CMP      AL,0FH
332 010F 74 06      JE       BOX_ON
333 0111 C7 06 006C R 01A4      MOV      TIMER_LOW,420D      ; CONTROLLER IS IN SYSTEM UNIT
334 0117          BOX_ON:
335 0117 BA 0213      MOV      DX,213H          ; EXPANSION BOX
336 011A B0 FF      MOV      AL,0FFH
337 011C EE          OUT      DX,AL          ; TURN BOX ON
338 011D B9 0001      MOV      CX,1          ; ATTEMPT NEXT DRIVES
339 0120 BA 0081      MOV      DX,081H
340 0123          P3:
341 0123 2B C0      SUB      AX,AX          ; RESET THE CONTROLLER
342 0125 CD 13      INT      13H
343 0127 72 42      JC       POD_DONE
344 0129 B8 1100      MOV      AX,01100H      ; RECALIBRATE THE DRIVE
345 012C CD 13      INT      13H
346 012E 73 0A      JNC      P5
347 0130 A1 006C R      MOV      AX,TIMER_LOW
348 0133 3D 01BE      CMP      AX,446D          ; 25 SECONDS
349 0136 72 EB      JB       P3
350 0138 EB 31      JMP      SHORT POD_DONE
351 013A          P5:
352 013A B8 0900      MOV      AX,0900H          ; INITIALIZE DRIVE CHARACTERISTICS
353 013D CD 13      INT      13H          ; FOR DRIVE 1
354 013F 72 2A      JC       POD_DONE
355 0141 FE 06 0075 R      INC      HF_NUM          ; TALLY ANOTHER DRIVE
356 0145 81 FA 0081      CMP      DX,(80H + S_MAX_FILE - 1)
357 0149 73 20      JAE      POD_DONE
358 014B 42          INC      DX
359 014C EB D5      JMP      P3
360
361 014E 31 37 30 31 0D 0A      F17      DB      '1701',0DH,0AH      ; POST MESSAGE
362 = 0006      F17L      EQU      $-F17
363
364          ;----- POD ERROR
365
366 0154          ERROR_EX:
367 0154 BD 000F      MOV      BP,0FH          ; POD ERROR FLAG
368 0157 2B F6      SUB      SI,SI
369 0159 B9 0006      MOV      CX,F17L          ; MESSAGE CHARACTER COUNT
370 015C B7 00      MOV      BH,0          ; PAGE ZERO
371 015E          OUT_CH:
372 015E 2E: 8A 84 014E R      MOV      AL,CS:F17[SI]      ; GET BYTE
373 0163 B4 0E      MOV      AH,14D          ; VIDEO OUT
374 0165 CD 10      INT      10H          ; DISPLAY CHARACTER
375 0167 46          INC      SI          ; NEXT CHAR
376 0168 E2 F4      LOOP     OUT_CH          ; DO MORE
377 016A F9          STC
378 016B          POD_DONE:
379 016B FA          CLI          ; NO INTERRUPTS
380 016C E4 21      IN      AL,INTA01      ; READ THE INTERRUPT MASK
381 016E 0C 01      OR      AL,01H          ; DISABLE THE TIMER
382 0170 E6 21      OUT      INTA01,AL
383 0172 FB          STI          ; ENABLE INTERRUPTS
384 0173 E8 0232 R      CALL     DSBL          ; DISABLE THE CARD MASKS
385 0176 CB          RET
386
387 0177          HD_RESET_1      PROC      NEAR
388 0177 51          PUSH     CX          ; SAVE REGISTER
389 0178 52          PUSH     DX
390 0179 B9 0100      MOV      CX,0100H          ; RETRY COUNT
391 017C          L6:
392 017C E8 076D R      CALL     PORT_0
393 017F 42          INC      DX          ; ADDRESS PORT_1
394 0180 EE          OUT      DX,AL          ; RESET CARD
395 0181 EB 00      JMP      $+2          ; I/O DELAY AT LEAST +5us
396 0183 EB 00      JMP      $+2          ; ALLOW TIME TO CLEAR THE
397 0185 EB 00      JMP      $+2          ; HARDWARE STATUS REGISTER
398 0187 EC          IN      AL,DX          ; READ THE HARDWARE STATUS
399 0188 24 3F      AND      AL,00111111B      ; MASK OFF UPPER 2 BITS AND CLEAR CY
400 018A 74 03      JZ       R3          ; EXIT IF REGISTER IS CLEARED WITH CY=0
401 018C E2 EE      LOOP     L6          ; TRY AGAIN
402 018E F9          STC          ; SET ERROR CONDITION CY=1
403 018F          R3:
404 018F 5A          POP      DX          ; RESTORE REGISTER
405 0190 59          POP      CX
406 0191 C3          RET
407 0192          HD_RESET_1      ENDP
408 0192          DISK_SETUP      ENDP

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409 PAGE
410 ;--- INT 19 H -----
411 ;
412 ; INTERRUPT 19 BOOT STRAP LOADER
413 ;
414 ; - THE FIXED DISK BIOS REPLACES THE INTERRUPT 19H BOOT
415 ; STRAP VECTOR WITH A POINTER TO THIS BOOT ROUTINE AND
416 ; RESETS THE DEFAULT DISK AND DISKETTE PARAMETER VECTORS
417 ;
418 ; - THE BOOT BLOCK TO BE READ IN WILL BE ATTEMPTED FROM
419 ; CYLINDER 0 SECTOR 1 OF THE DEVICE.
420 ;
421 ; - THE BOOTSTRAP SEQUENCE IS:
422 ; ATTEMPT TO LOAD FROM THE DISKETTE INTO THE BOOT
423 ; LOCATION (0000:7C00H) WHERE CONTROL IS TRANSFERRED.
424 ; IF THE DISKETTE FAILS THE FIXED DISK IS TRIED FOR A
425 ; VALID BOOTSTRAP BLOCK. A VALID BOOT BLOCK ON THE
426 ; FIXED DISK CONSISTS OF THE BYTES 055H 0AAH AS THE
427 ; LAST TWO BYTES OF THE BLOCK.
428 ; IF THE ABOVE FAILS CONTROL IS PASSED TO RESIDENT BASIC
429 ;
430 ;-----
431
432 0192 BOOT_STRAP:
433 ASSUME DS:ABS0,ES:ABS0
434 0192 2B C0 SUB AX,AX
435 0194 8E D8 MOV DS,AX ; ESTABLISH SEGMENT
436 0196 B4 C0 MOV AH,0C0H
437 0198 CD 15 INT 15H ; READ CONFIGURATION PARAMETERS
438 ; IF XT OR PC, INTERRUPTS ARE DISABLED
439 ; AT THIS POINT.
440 ;----- RESET PARAMETER VECTORS
441 019A FA CLI
442 019B C7 06 0104 R 03FF R MOV WORD PTR HF_TBL_VEC,OFFSET FD_TBL
443 01A1 8C 0E 0106 R MOV WORD PTR HF_TBL_VEC+2,CS
444 01A5 73 0A JNC H0 ; JMP IF INT 15 FUNCTION IMPLEMENTED
445
446 01A7 C7 06 0078 R 0227 R MOV WORD PTR DISKETTE_PARM,OFFSET DISKETTE_TBL
447 01AD 8C 0E 007A R MOV WORD PTR DISKETTE_PARM+2,CS
448 01B1 H0:
449 01B1 FB STI
450
451 ;----- ATTEMPT BOOTSTRAP FROM DISKETTE
452
453 01B2 2B D2 SUB DX,DX ; DRIVE ZERO
454
455 ;----- ESTABLISH ES:BX POINTER
456
457 01B4 8E C2 MOV ES,DX ; ESTABLISH SEGMENT
458 01B6 BB 7C00 R MOV BX,OFFSET BOOT_LOCN ; SET BX TO 7C00H
459
460 ;----- CLEAR BOOT_LOCN
461
462 01B9 FC CLD ; DIRECTION FORWARD
463 01BA 33 C0 XOR AX,AX
464 01BC B9 0100 MOV CX,256 ; CLEAR 256 WORDS
465 01BF 8B FB MOV DI,BX ; POINT TO BOOT LOCATION BUFFER
466 01C1 F3 AB REP STOSW ; ZERO THE BOOT LOCATION BUFFER
467
468 01C3 B9 0004 MOV CX,4 ; SET RETRY COUNT
469 01C6 H1:
470 01C6 51 PUSH CX ; SAVE RETRY COUNT
471 01C7 2B C0 SUB AX,AX ; RESET THE DISKETTE
472 01C9 CD 13 INT 13H ; FILE IO CALL
473 01CB 72 08 JC H2 ; IF ERROR, TRY AGAIN
474
475 01CD B8 0201 MOV AX,0201H ; READ IN THE SINGLE SECTOR
476 01D0 B9 0001 MOV CX,1 ; SECTOR 1, TRACK 0,
477 01D3 CD 13 INT 13H ; FILE IO CALL
478 01D5 59 POP CX ; RECOVER RETRY COUNT
479 01D6 73 09 JNC H3 ; CARRY FLAG SET BY UNSUCCESSFUL READ
480
481 01D8 80 FC 80 CMP AH,80H ; IF TIME OUT, NO RETRY
482 01DB 74 22 JZ H6 ; TRY FIXED DISK
483
484 01DD E2 E7 LOOP H1 ; DO IT FOR RETRY TIMES
485 01DF EB 1E JMP SHORT H6 ; UNABLE TO IPL FROM THE DISKETTE
486
487 01E1 80 3E 7C00 R 06 H3: CMP BYTE PTR BOOT_LOCN,06H ; CHECK FOR FIRST INSTRUCTION INVALID
488 01E6 72 3D JB H10 ; IF BOOT NOT VALID, GO TO BASIC
489
490 ;----- INSURE DATA PATTERN FIRST 8 WORDS NOT ALL EQUAL
491
492 01E8 BF 7C00 R MOV DI,OFFSET BOOT_LOCN ; CHECK DATA PATTERN
493 01EB B9 0008 MOV CX,8 ; CHECK THE NEXT 8 WORDS
494 01EE A1 7C00 R MOV AX,WORD PTR BOOT_LOCN ; LOAD THE FIRST WORD
495
496 01F1 83 C7 02 H4: ADD DI,2 ; POINT TO NEXT WORD
497 01F4 3B 05 CMP AX,[DI] ; CHECK DATA PATTERN FOR A FILL PATTERN
498 01F6 E1 F9 LOOPZ H4
499 01F8 74 2B JZ H10 ; BOOT NOT VALID, GO TO BASIC
500 01FA H5:
501 01FA EA 7C00 ---- R JMP BOOT_LOCN
502
503 ;----- ATTEMPT BOOTSTRAP FROM FIXED DISK
504
505 01FF H6:
506 01FF 2B C0 SUB AX,AX ; RESET DISKETTE
507 0201 CD 13 INT 13H
508 0203 B9 0003 MOV CX,3 ; SET RETRY COUNT
509 0206 BA 0080 MOV DX,0080H ; FIXED DISK ZERO
510 0209 H7:
511 0209 51 PUSH CX ; SAVE RETRY COUNT
512 020A 2B C0 SUB AX,AX ; RESET THE FIXED DISK
513 020C CD 13 INT 13H ; FILE IO CALL
514 020E 72 08 JC H8 ; IF ERROR, TRY AGAIN
515
516 ;----- ES AND BX ALREADY ESTABLISHED
517
518 0210 B8 0201 MOV AX,0201H ; READ IN THE SINGLE SECTOR
519 0213 B9 0001 MOV CX,1 ; SECTOR 1, TRACK 0
520 0216 CD 13 INT 13H ; FILE IO CALL
521 0218 59 POP CX ; RECOVER RETRY COUNT
522 0219 72 08 JC H9

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523 021B A1 7DFE R      MOV     AX,WORD PTR BOOT_LOCN+510D
524 021E 3D AA55        CMP     AX,0AA55H      ; TEST FOR GENERIC BOOT BLOCK
525 0221 74 D7          JZ      H5              ; GO TO BOOT LOCATION
526 0223                H9:
                    LOOP     H7              ; DO IT FOR RETRY TIMES
527 0223 E2 E4
528
529                ;----- UNABLE TO IPL FROM THE DISKETTE OR FIXED DISK
530
531 0225                H10:
532 0225 CD 18          INT     18H            ; RESIDENT BASIC
533
534 0227                DISKETTE_TEL:
535
536 0227 CF              DB      11001111B      ; SRT=D, HD UNLOAD=OF - 1ST SPEC BYTE
537 0228 02              DB      2              ; HD LOAD=1, MODE=DMA - 2ND SPEC BYTE
538 0229 25              DB      25H           ; MOTOR TIMEOUT AFTER OPERATION
539 022A 02              DB      2              ; 512 BYTES PER SECTOR
540 022B 08              DB      8              ; EOT (LAST SECTOR ON TRACK)
541 022C 2A              DB      02AH          ; GAP LENGTH
542 022D FF              DB      0FFH          ; OTL
543 022E 50              DB      050H          ; GAP LENGTH FOR FORMAT
544 022F F6              DB      0F6H          ; FILL BYTE FOR FORMAT
545 0230 19              DB      25            ; HEAD SETTLE TIME (MILLISECONDS)
546 0231 04              DB      4              ; MOTOR START TIME (1/8 SECOND)
547
548                ;----- MAKE SURE THAT ALL HOUSEKEEPING IS DONE BEFORE EXIT
549
550 0232                DSBL    PROC     NEAR
551 0232 2A C0            SUB     AL,AL          ; RESET INT/DMA MASK
552 0234 BA 0323         MOV     DX,HF_PORT+3    ; LOAD FOR PORT_ADDRESS 3
553 0237 FA              CLI              ; DISABLE INTERRUPTS
554 0238 EE              OUT     DX,AL          ; RESET INT/DMA MASK CARD 0
555 0239 83 C2 04        ADD     DX,4
556 023C EE              OUT     DX,AL          ; RESET INT/DMA MASK CARD 1
557 023D 83 C2 04        ADD     DX,4
558 0240 EE              OUT     DX,AL          ; RESET INT/DMA MASK CARD 2
559 0241 83 C2 04        ADD     DX,4
560 0244 EE              OUT     DX,AL          ; RESET INT/DMA MASK CARD 3
561
562 0245 B0 07            MOV     AL,07H
563 0247 E6 0A            OUT     DMA+10,AL      ; SET DMA MODE TO DISABLE TG
564 0249 E4 21            IN      AL,INTA01
565 024B 0C 20            OR      AL,020H
566 024D E6 21            OUT     INTA01,AL     ; DISABLE IREQ 5
567 024F FB              STI              ; ENABLE INTERRUPTS
568 0250 C3              RET
569 0251                DSBL    ENDP
570
571                ;--- DISK_IO -----
572                ;
573                ;          FIXED DISK BIOS ENTRY POINT
574                ;
575                ;-----
576
577 0251                DISK_IO PROC     FAR
578                ASSUME DS:DATA,ES:NOTHING
579 0251 80 FA 80          CMP     DL,080H      ; TEST FOR FIXED DISK DRIVE
580 0254 73 05            JAE     HARD_DISK    ; YES, HANDLE HERE
581 0256 CD 40            INT     40H          ; DISKETTE HANDLER
582 0258
583 0258 CA 0002         RET     2              ; BACK TO CALLER
584
585 025B                HARD_DISK:
586 025B FB              STI              ; ENABLE INTERRUPTS
587 025C 0A E4            OR      AH,AH
588 025E 75 09            JNZ     A3
589 0260 CD 40            INT     40H          ; RESET NEC WHEN AH=0
590 0262 2A E4            SUB     AH,AH
591 0264 80 FA 81          CMP     DL,(80H+S_MAX_FILE-1) ; DL IN LIMITS?
592 0267 77 EF            JA      RET_2
593 0269
594 0269 80 FC 08          CMP     AH,8          ; GET PARAMETERS IS A SPECIAL CASE
595 026C 75 03            JNZ     A2
596 026E E9 0380 R        JMP     GET_FARM_N
597 0271
598 0271 55                A2:
599 0272 8B EC            PUSH    BP          ; SAVE THE BASE POINTER
600 0274 83 EC 08        MOV     BP,SP        ; LOAD THE CMD_BLOCK POINTER
601
602 0277 53                SUB     SP,8        ; ALLOCATE SPACE FOR THE COMMAND BLOCK
603 0278 51                ; ON THE STACK.
604 0279 52                ; SAVE REGISTERS DURING OPERATION
605 027A 1E              PUSH    BX
606 027B 06              PUSH    CX
607 027C 56              PUSH    DX
608 027D 57              PUSH    DS
609 027E BE ---- R        PUSH    ES
610 0281 8E DE            PUSH    SI
611
612 0283 E8 02D0 R        PUSH    DI
613
614 0286 50                MOV     SI,DATA
615 0287 E8 0232 R        MOV     DS,SI      ; ESTABLISH DATA SEGMENT
616 028A B8 ---- R        MOV     DS,SI
617 028D 8E D8            ; ESTABLISH SEGMENT
618 028F 58              POP     AX
619 0290 8A 26 0074 R      POP     AH,DISK_STATUS ; RESTORE THE REGISTERS
620 0294 5F              POP     DI
621 0295 5E              POP     SI
622 0296 07              POP     ES
623 0297 1F              POP     DS
624 0298 5A              POP     DX
625 0299 59              POP     CX
626 029A 5B              POP     BX
627
628 029B 83 C4 08          ADD     SP,8          ; ADJUST FOR THE COMMAND BLOCK.
629 029E 5D              POP     BP          ; RESTORE BASE POINTER
630 029F 80 FC 01          CMP     AH,1          ; SET THE CARRY FLAG TO INDICATE
631 02A2 F5              CMC              ; SUCCESS OR FAILURE
632 02A3 CA 0002         RET     2              ; THROW AWAY SAVED FLAGS
633 02A6                DISK_IO ENDP

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634                                     PAGE
635 02A6                                M1 LABEL WORD ; FUNCTION TRANSFER TABLE
636 02A6 032E R                        DW DISK_RESET ; 000H
637 02A8 0347 R                        DW RETURN_STATUS ; 001H
638 02AA 0350 R                        DW DISK_READ ; 002H
639 02AC 0359 R                        DW DISK_WRITE ; 003H
640 02AE 0362 R                        DW DISK_VERIFY ; 004H
641 02B0 0369 R                        DW FMT_TRK ; 005H
642 02B2 036F R                        DW FMT_BAD ; 006H
643 02B4 0375 R                        DW FMT_DRV ; 007H
644 02B6 0326 R                        DW BAD_COMMAND ; 008H
645 02B8 043F R                        DW INIT_DRV ; 009H
646 02BA 04F4 R                        DW RD_LONG ; 00AH
647 02BC 0501 R                        DW WR_LONG ; 00BH
648 02BE 0515 R                        DW DISK_SEEK ; 00CH
649 02C0 032E R                        DW DISK_RESET ; 00DH
650 02C2 051B R                        DW RD_BUFF ; 00EH
651 02C4 0527 R                        DW WR_BUFF ; 00FH
652 02C6 0533 R                        DW TST_RDY ; 010H
653 02C8 0539 R                        DW HDISK_RECAL ; 011H
654 02CA 053F R                        DW RAM_DIAG ; 012H
655 02CC 0545 R                        DW CHK_DRV ; 013H
656 02CE 054B R                        DW CNTLR_DIAG ; 014H
657 = 002A                                M1L EQU $-M1
658
659 02D0                                DISK_IO_CONT PROC NEAR
660 02D0 80 FC 01                        CMP AH,01H ; RETURN STATUS
661 02D3 74 72                        JE RETURN_STATUS
662
663 02D5 80 EA 80                        SUB DL,080H ; CONVERT DRIVE NUMBER TO 0 BASED RANGE
664 02D8 80 FA 08                        CMP DL,MAX_FILE ; LEGAL DRIVE TEST
665 02DB 73 49                        JAE BAD_COMMAND
666
667 02DD C6 06 0074 R 00                MOV DISK_STATUS,0 ; RESET THE STATUS INDICATOR
668
669 ;----- SET UP COMMAND BLOCK
670
671 02E2 FE C9                        DEC CL ; SECTORS 0-16 FOR CONTROLLER
672 02E4 C6 46 F8 00                MOV CMD_BLOCK+0,0 ; SET TO ZERO THE OP CODE
673 02E8 88 4E FA                        MOV CMD_BLOCK+2,CL ; SECTOR AND HIGH 2 BITS CYLINDER
674 02EB 88 6E FB                        MOV CMD_BLOCK+3,CH ; CYLINDER LOW
675 02EE 88 46 FC                        MOV CMD_BLOCK+4,AL ; INTERLEAVE / BLOCK COUNT
676 02F1 A0 0076 R                MOV AL,CONTROL_BYTE ; CONTROL BYTE (STEP OPTION)
677 02F4 88 46 FD                        MOV CMD_BLOCK+5,AL ; SET THE CONTROL FIELD
678
679 ;----- CALCULATE THE PORT OFFSET
680
681 02F7 8A EA                        MOV CH,DL ; SAVE DL
682 02F9 80 CA 01                        OR DL,1
683 02FC FE CA                        DEC DL
684 02FE D0 E2                        SHL DL,1 ; GENERATE OFFSET
685 0300 88 16 0077 R                MOV PORT_OFF,DL ; STORE OFFSET
686 0304 8A D5                        MOV DL,CH ; RESTORE DL
687 0306 80 E2 01                        AND DL,1 ; MAKE DRIVE 0 OR 1
688 0309 B1 05                        MOV CL,5 ; SHIFT COUNT
689 030B D2 E2                        SHL DL,CL ; DRIVE NUMBER (0,1)
690 030D 0A D6                        OR DL,DH ; HEAD NUMBER
691 030F 88 56 F9                        MOV CMD_BLOCK+1,DL ; SET THE DRIVE AND HEAD
692
693 0312 8B C8                        MOV CX,AX ; CALCULATE JUMP ADDRESS
694 0314 8A CD                        MOV CL,CH ; GET INTO LOW BYTE
695 0316 32 ED                        XOR CH,CH ; ZERO HIGH BYTE
696 0318 D1 E1                        SAL CX,1 ; *2 FOR TABLE LOOKUP
697 031A 8B F1                        MOV SI,CX ; PUT INTO SI FOR BRANCH
698 031C 83 F9 2A                        CMP CX,M1L ; TEST WITHIN RANGE
699 031F 73 05                        JNB BAD_COMMAND
700 0321 2E: FF A4 02A6 R                JMP WORD PTR CS:[SI+OFFSET M1] ; GO DO THE COMMAND
701 0326                                BAD_COMMAND:
702 0326 C6 06 0074 R 01                MOV DISK_STATUS,BAD_CMD ; SET BAD COMMAND ERROR
703 032B B0 00                        MOV AL,0
704 032D C3                        RET ; EXIT
705 032E                                DISK_IO_CONT ENDP
706
707 ;-----
708 ; RESET THE DISK SYSTEM (AH = 000H) :
709 ;-----
710
711 032E                                DISK_RESET PROC NEAR
712 032E E8 076D R                        CALL PORT_0 ; RESET PORT
713 0331 42                        INC DX ; PORT_1 ADDRESS
714 0332 EE                        OUT DX,AL ; RESET CARD
715 0333 EB 00                        JMP $+2 ; I/O DELAY AT LEAST +5us
716 0335 EB 00                        JMP $+2 ; ALLOW TIME TO CLEAR THE
717 0337 EB 00                        JMP $+2 ; HARDWARE STATUS REGISTER
718 0339 EC                        IN AL,DX ; READ THE HARDWARE STATUS
719 033A 24 3F                        AND AL,00111111B ; MASK OFF UPPER 2 BITS AND CLEAR CY
720 033C 74 06                        JZ DRI ; EXIT IF REGISTER IS CLEARED WITH CY=0
721 033E C6 06 0074 R 05                MOV DISK_STATUS,BAD_RESET ; SET THE ERROR CONDITION
722 0343 C3                        RET ; EXIT
723 0344                                DRI:
724 0344 E9 043F R                        JMP INIT_DRV ; SET THE DRIVE PARAMETERS
725
726 0347                                DISK_RESET ENDP
727
728 ;-----
729 ; DISK STATUS ROUTINE (AH = 001H) :
730 ;-----
731
732 0347                                RETURN_STATUS PROC NEAR
733 0347 A0 0074 R                        MOV AL,DISK_STATUS ; OBTAIN PREVIOUS STATUS
734 034A C6 06 0074 R 00                MOV DISK_STATUS,0 ; RESET STATUS
735 034F C3                        RET
736 0350                                RETURN_STATUS ENDP
737
738 ;-----
739 ; DISK READ ROUTINE (AH = 002H) :
740 ;-----
741
742 0350                                DISK_READ PROC NEAR
743 0350 B0 47                        MOV AL,DMA_READ ; MODE BYTE FOR DMA READ
744 0352 C6 46 F8 08                MOV CMD_BLOCK+0,READ_CMD
745 0356 E9 055E R                        JMP DMA_OPN
746 0359                                DISK_READ ENDP
747

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748 ;-----
749 ; DISK WRITE ROUTINE (AH = 003H) :
750 ;-----
751
752 0359 DISK_WRITE PROC NEAR
753 0359 B0 4B MOV AL,DMA_WRITE ; MODE BYTE FOR DMA WRITE
754 035B C6 46 F8 0A MOV CMD_BLOCK+0,WRITE_CMD
755 035F E9 055E R JMP DMA_OPN
756 0362 DISK_WRITE ENDP
757
758 ;-----
759 ; DISK VERIFY (AH = 004H) :
760 ;-----
761
762 0362 DISK_VERF PROC NEAR
763 0362 C6 46 F8 05 MOV CMD_BLOCK+0,CHK_TRK_CMD
764 0366 E9 054F R JMP NDMA_OPN
765 0369 DISK_VERF ENDP
766
767 ;-----
768 ; FORMATTING (AH = 005H 006H C07H) :
769 ;-----
770
771 0369 FMT_TRK PROC NEAR ; FORMAT TRACK (AH = 005H)
772 0369 C6 46 F8 06 MOV CMD_BLOCK+0,FMTTRK_CMD
773 036D EB 0A JMP SHORT FMT_CONT
774 036F FMT_TRK ENDP
775
776 036F FMT_BAD PROC NEAR ; FORMAT BAD TRACK (AH = 006H)
777 036F C6 46 F8 07 MOV CMD_BLOCK+0,FMTBAD_CMD
778 0373 EB 04 JMP SHORT FMT_CONT
779 0375 FMT_BAD ENDP
780
781 0375 FMT_DRV PROC NEAR ; FORMAT DRIVE (AH = 007H)
782 0375 C6 46 F8 04 MOV CMD_BLOCK+0,FMTDRV_CMD
783 0379 FMT_DRV ENDP
784
785 0379 FMT_CONT:
786 0379 80 66 FA C0 AND CMD_BLOCK+2,11000000B ; ZERO OUT SECTOR FIELD
787 037D E9 054F R JMP NDMA_OPN
788
789 ;-----
790 ; GET PARAMETERS (AH = 8) :
791 ;-----
792
793 0380 GET_PARM_N LABEL NEAR
794 0380 GET_PARM PROC FAR ; GET DRIVE PARAMETERS
795 0380 1E PUSH DS ; SAVE REGISTERS
796 0381 06 PUSH ES
797 0382 53 PUSH BX
798
799 ASSUME DS:ABS0
800 0383 2B C0 SUB AX,AX ; ESTABLISH ADDRESSING
801 0385 8E D8 MOV DS,AX
802 0387 C4 1E 0104 R LES BX,HF_TBL_VEC
803
804 ASSUME DS:DATA
805 038B B8 ---- R MOV AX,DATA
806 038E 8E D8 MOV DS,AX ; ESTABLISH SEGMENT
807 0390 80 EA 80 SUB DL,80H
808 0393 80 FA 08 CMP DL,MAX_FILE ; TEST WITHIN RANGE
809 0396 73 57 JAE G4
810 0398 C6 06 0074 R 00 MOV DISK_STATUS,0 ; RESET THE STATUS INDICATOR
811 039D 8A EA MOV CH,DL ; SAVE THE DRIVE
812 039F 80 CA 01 OR DL,1
813 03A2 FE CA DEC DL
814 03A4 D0 E2 SHL DL,1 ; GENERATE OFFSET
815 03A6 88 16 0077 R MOV PORT_OFF,DL ; STORE OFFSET
816 03AA 8A D5 MOV DL,CH ; RESTORE DL
817 03AC 80 E2 01 AND DL,00000001B ; DRIVE 0 OR DRIVE 1
818 03AF 8A E2 MOV AH,DL
819 03B1 E8 076D R CALL PORT_0
820 03B4 42 INC DX ; PORT_2 ADDRESS
821 03B5 42 INC DX
822 03B6 EC IN AL,DX ; READ SWITCH SETTINGS
823 03B7 80 FC 00 CMP AH,0 ; DRIVE 0 OR 1
824 03BA 75 04 JNZ G0
825 03BC D0 E8 SHR AL,1 ; RIGHT JUSTIFY THE SWITCH BITS
826 03BE D0 E8 SHR AL,1
827 03C0 G0:
828 03C0 24 03 AND AL,00000011B ; ISOLATE THE TABLE BITS
829 03C2 B1 04 MOV CL,4 ; TABLE LENGTH IS 16 BYTES
830 03C4 D2 E0 SHL AL,CL ; ADJUST
831 03C6 2A E4 SUB AH,AH
832 03C8 03 D8 ADD BX,AX
833 03CA 26: 8B 07 MOV AX,ES:[BX] ; MAX NUMBER OF CYLINDERS
834 03CD 2D 0002 SUB AX,2 ; ADJUST FOR 0-N
835 ; AND RESERVE LAST TRACK
836 03D0 8A E8 MOV CH,AL
837 03D2 25 0300 AND AX,0300H ; HIGH TWO BITS OF CYLINDER
838 03D5 D1 E8 SHR AX,1
839 03D7 D1 E8 SHR AX,1
840 03D9 0C 11 OR AL,011H ; SECTORS
841 03DB 8A C8 MOV CL,AL
842 03DD 26: 8A 77 02 MOV DH,ES:[BX][2] ; HEADS
843 03E1 FE CE DEC DH ; 0-N RANGE
844 03E3 8A 16 0075 R MOV DL,HF_NUM ; DRIVE COUNT
845 03E7 2B C0 SUB AX,AX
846 03E9 G5:
847 03E9 5B POP BX ; RESTORE REGISTERS
848 03EA 07 POP ES
849 03EB 1F POP DS
850 03EC CA 0002 RET 2 ; EXIT
851 03EF G4:
852 03EF C6 06 0074 R 07 MOV DISK_STATUS,INIT_FAIL ; OPERATION FAILED
853 03F4 B4 07 MOV AH,INIT_FAIL
854 03F6 2A C0 SUB AL,AL
855 03F8 2B D2 SUB DX,DX
856 03FA 2B C9 SUB CX,CX
857 03FC F9 STC ; SET ERROR FLAG
858 03FD EB EA JMP G5 ; EXIT
859 03FF GET_PARM ENDP

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860 PAGE
861 ;-----
862 ; INITIALIZE DRIVE CHARACTERISTICS :
863 ; :
864 ; FIXED DISK PARAMETER TABLE: :
865 ; :
866 ; - THE TABLE IS COMPOSED OF A BLOCK DEFINED AS: :
867 ; :
868 ; (1 WORD) - MAXIMUM NUMBER OF CYLINDERS :
869 ; (1 BYTE) - MAXIMUM NUMBER OF HEADS :
870 ; (1 WORD) - STARTING REDUCED WRITE CURRENT CYL :
871 ; (1 WORD) - STARTING WRITE PRECOMPENSATION CYL :
872 ; (1 BYTE) - MAXIMUM ECC DATA BURST LENGTH :
873 ; (1 BYTE) - CONTROL BYTE (DRIVE STEP OPTION) :
874 ; BIT 7 DISABLE DISK-ACCESS RETRIES :
875 ; BIT 6 DISABLE ECC RETRIES :
876 ; BITS 5-3 ZERO :
877 ; BITS 2-0 DRIVE OPTION :
878 ; (1 BYTE) - STANDARD TIME OUT VALUE (SEE BELOW) :
879 ; (1 BYTE) - TIME OUT VALUE FOR FORMAT DRIVE :
880 ; (1 BYTE) - TIME OUT VALUE FOR CHECK DRIVE :
881 ; (1 WORD) - LANDING ZONE :
882 ; (1 BYTE) - SECTORS/TRACK :
883 ; (1 BYTE) - RESERVED FOR FUTURE USE :
884 ; :
885 ; - TO DYNAMICALLY DEFINE A SET OF PARAMETERS :
886 ; BUILD A TABLE OF VALUES AND PLACE THE :
887 ; CORRESPONDING VECTOR INTO INTERRUPT 41. :
888 ; :
889 ; NOTE: THE DEFAULT TABLE IS VECTORED IN FOR :
890 ; AN INTERRUPT 19H (BOOTSTRAP) :
891 ; :
892 ; ON THE CARD SWITCH SETTINGS :
893 ; :
894 ; DRIVE 0 DRIVE 1 :
895 ; :
896 ; ON : -1- -2- / -3- -4- :
897 ; :
898 ; OFF : / :
899 ; :
900 ; :
901 ; TRANSLATION TABLE :
902 ; :
903 ; DRIVE 0 : DRIVE 1 : TABLE ENTRY :
904 ; 1/2 : 3/4 : :
905 ; :
906 ; ON ON : ON ON : 0 :
907 ; ON OFF : ON OFF : 1 :
908 ; OFF ON : OFF ON : 2 :
909 ; OFF OFF : OFF OFF : 3 :
910 ; :
911 ;-----
912
913 03FF FD_TBL:
914
915 ;----- DRIVE TABLE 0
916
917 03FF 0132 DW 0306D ; MAX CYLINDERS
918 0401 04 DB 04D ; MAX HEADS
919 0402 0132 DW 0306D ; START REDUCED WRITE CURRENT CYL
920 0404 0000 DW 0 ; START WRITE PRECOMPENSATION CYL
921 0406 0B DB 0BH ; MAX ECC BURST DATA LENGTH
922 0407 05 DB 00000101B ; CONTROL BYTE
923 0408 10 DB 010H ; STANDARD TIME OUT
924 0409 C0 DB 0C0H ; TIME OUT FOR FORMAT DRIVE
925 040A 28 DB 028H ; TIME FOR CHECK DRIVE
926 040B 0132 DW 0306D ; LANDING ZONE
927 040D 11 DB 017D ; SECTORS/TRACK
928 040E 00 DB 0 ; RESERVED
929
930 ;----- DRIVE TABLE 1
931
932 040F 0264 DW 0612D ; MAX CYLINDERS
933 0411 04 DB 04D ; MAX HEADS
934 0412 0264 DW 0612D ; START REDUCED WRITE CURRENT CYL
935 0414 0000 DW 0 ; START WRITE PRECOMPENSATION CYL
936 0416 0B DB 0BH ; MAX ECC BURST DATA LENGTH
937 0417 05 DB 00000101B ; CONTROL BYTE
938 0418 28 DB 028H ; STANDARD TIME OUT
939 0419 E0 DB 0E0H ; TIME OUT FOR FORMAT DRIVE
940 041A 42 DB 042H ; TIME FOR CHECK DRIVE
941 041B 0297 DW 0663D ; LANDING ZONE
942 041D 11 DB 017D ; SECTORS/TRACK
943 041E 00 DB 0 ; RESERVED
944
945 ;----- DRIVE TABLE 2
946
947 041F 0267 DW 0615D ; MAX CYLINDERS
948 0421 04 DB 04D ; MAX HEADS
949 0422 0267 DW 0615D ; START REDUCED WRITE CURRENT CYL
950 0424 012C DW 0300D ; START WRITE PRECOMPENSATION CYL
951 0426 0B DB 0BH ; MAX ECC BURST DATA LENGTH
952 0427 05 DB 00000101B ; CONTROL BYTE
953 0428 28 DB 028H ; STANDARD TIME OUT
954 0429 E0 DB 0E0H ; TIME OUT FOR FORMAT DRIVE
955 042A 42 DB 042H ; TIME FOR CHECK DRIVE
956 042B 0267 DW 0615D ; LANDING ZONE
957 042D 11 DB 017D ; SECTORS/TRACK
958 042E 00 DB 0 ; RESERVED
959
960 ;----- DRIVE TABLE 3
961
962 042F 0132 DW 0306D ; MAX CYLINDERS
963 0431 08 DB 08D ; MAX HEADS
964 0432 0132 DW 0306D ; START REDUCED WRITE CURRENT CY
965 0434 0080 DW 0128D ; START WRITE PRECOMPENSATION CYL
966 0436 0B DB 0BH ; MAX ECC BURST DATA LENGTH
967 0437 05 DB 00000101B ; CONTROL BYTE
968 0438 28 DB 028H ; STANDARD TIME OUT
969 0439 E0 DB 0E0H ; TIME OUT FOR FORMAT DRIVE
970 043A 42 DB 042H ; TIME FOR CHECK DRIVE
971 043B 0150 DW 0336D ; LANDING ZONE
972 043D 11 DB 017D ; SECTORS/TRACK
973 043E 00 DB 0 ; RESERVED

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974
975
976 ;-----
977 ;      INITIALIZE DRIVE                      (AH = 09H) :
978 ;-----
979 043F      INIT_DRV      PROC      NEAR
980
981 ;----- DO DRIVE ZERO
982
983 043F C6 46 F8 0C      MOV      CMD_BLOCK+0,INIT_DRV_CMD
984 0443 C6 46 F9 00      MOV      CMD_BLOCK+1,0      ; SET FOR DRIVE 0
985 0447 E8 0458 R      CALL      INIT_DRV_R      ; SEND THE PARAMETERS
986 044A 72 0B      JC      INIT_DRV_OUT      ; ERROR?
987
988 ;----- DO DRIVE ONE
989
990 044C C6 46 F8 0C      MOV      CMD_BLOCK+0,INIT_DRV_CMD
991 0450 C6 46 F9 20      MOV      CMD_BLOCK+1,00100000B      ; SET TO DRIVE 1
992 0454 E8 0458 R      CALL      INIT_DRV_R      ; SEND THE PARAMETERS
993 0457      INIT_DRV_OUT:
994 0457 C3      RET      ; EXIT
995 0458      INIT_DRV      ENDP
996
997 0458      INIT_DRV_R      PROC      NEAR
998 0458 2A C0      SUB      AL,AL
999 045A E8 057C R      CALL      COMMAND      ; ISSUE THE COMMAND
1000 045D 73 01      JNC      B1      ; DX = PORT 0 AFTER CALL
1001 045F C3      RET
1002 0460      B1:
1003 0460 8C D9      MOV      CX,DS      ; SAVE SEGMENT
1004
1005      ASSUME DS:ABS0
1006 0462 2B C0      SUB      AX,AX
1007 0464 8E D8      MOV      DS,AX      ; ESTABLISH SEGMENT
1008 0466 C4 1E 0104 R      LES      BX,HF_TBL_VEC      ; LOAD THE TABLE VECTOR
1009 046A 8E D9      MOV      DS,CX      ; RESTORE SEGMENT
1010
1011      ASSUME DS:DATA
1012 ;-----:
1013 ;      DETERMINE PARAMETER TABLE OFFSET      :
1014 ;      USING CONTROLLER PORT TWO AND      :
1015 ;      DRIVE NUMBER SPECIFIER (0-1)      :
1016 ;-----:
1017 046C 42      INC      DX
1018 046D 42      INC      DX      ; ADDRESS PORT 2
1019 046E EC      IN      AL,DX      ; READ THE SWITCH SETTINGS
1020 046F 8A 66 F9      MOV      AH,CMD_BLOCK+1
1021 0472 80 E4 20      AND      AH,00100000B      ; DRIVE 0 OR 1
1022 0475 75 04      JNZ      B2
1023 0477 D0 E8      SHR      AL,1      ; ADJUST
1024 0479 D0 E8      SHR      AL,1
1025 047B      B2:
1026 047B 24 03      AND      AL,011B      ; ISOLATE
1027 047D B1 04      MOV      CL,4
1028 047F D2 E0      SHL      AL,CL      ; ADJUST
1029 0481 2A E4      SUB      AH,AH
1030 0483 03 D8      ADD      BX,AX
1031 0485 B4 09      MOV      AH,00001001B      ; SET MASK FOR DATA MODE CPU TO CARD
1032
1033 ;----- SEND DRIVE PARAMETERS MOST SIGNIFICANT BYTE FIRST
1034
1035 0487 BF 0001      MOV      DI,1      ; SEND MSB OF MAX CYLINDER
1036 048A E8 04E9 R      CALL      INIT_DRV_S
1037 048D 72 4C      JC      B3
1038
1039 048F BF 0000      MOV      DI,0      ; SEND LSB OF MAX CYLINDER
1040 0492 E8 04E9 R      CALL      INIT_DRV_S
1041 0495 72 44      JC      B3
1042
1043 0497 BF 0002      MOV      DI,2      ; SEND THE MAXIMUM HEADS
1044 049A E8 04E9 R      CALL      INIT_DRV_S
1045 049D 72 3C      JC      B3
1046
1047 049F BF 0004      MOV      DI,4      ; SEND MSB OF REDUCE WRITE CURRENT
1048 04A2 E8 04E9 R      CALL      INIT_DRV_S      ; CYLINDER
1049 04A5 72 34      JC      B3
1050
1051 04A7 BF 0003      MOV      DI,3      ; SEND LSB OF REDUCE WRITE CURRENT
1052 04AA E8 04E9 R      CALL      INIT_DRV_S      ; CYLINDER
1053 04AD 72 2C      JC      B3
1054
1055 04AF BF 0006      MOV      DI,6      ; SEND MSB OF WRITE PRECOMP CYLINDER
1056 04B2 E8 04E9 R      CALL      INIT_DRV_S
1057 04B5 72 24      JC      B3
1058
1059 04B7 BF 0005      MOV      DI,5      ; SEND LSB OF WRITE PRECOMP CYLINDER
1060 04BA E8 04E9 R      CALL      INIT_DRV_S
1061 04BD 72 1C      JC      B3
1062
1063 04BF BF 0007      MOV      DI,7      ; SEND ECC BURST LENGTH
1064 04C2 E8 04E9 R      CALL      INIT_DRV_S
1065 04C5 72 14      JC      B3
1066
1067 04C7 BF 0008      MOV      DI,8      ; LOAD THE CONTROL BYTE AND PLACE IN
1068 04CA 26: 8A 01      MOV      AL,ES:[BX+DI]      ; MEMORY AT 40:76H
1069 04CD A2 0076 R      MOV      CONTROL_BYTE,AL
1070
1071 04D0 2B C9      SUB      CX,CX
1072 04D2 B4 0F      MOV      AH,00001111B      ; SET THE MASK FOR STATUS MODE
1073 04D4      B5:
1074 04D4 E8 068D R      CALL      HD_WAIT      ; GO WAIT FOR THE STATE TO HAPPEN
1075 04D7 73 09      JNC      B6      ; JMP TO READ THE STATUS BYTE
1076 04D9 E2 F9      LOOP     B5      ; TRY AGAIN
1077 04DB      B3:
1078 04DB C6 06 0074 R 07      MOV      DISK_STATUS,INIT_FAIL      ; OPERATION FAILED
1079 04E0 F9      STC      ; SET THE ERROR CONDITION
1080 04E1 C3      RET
1081 04E2      B6:
1082 04E2 4A      DEC      DX      ; ADDRESS PORT 0
1083 04E3 EC      IN      AL,DX      ; READ STATUS BYTE OF THE OPERATION
1084 04E4 24 02      AND      AL,2      ; MASK ERROR BIT
1085 04E6 75 F3      JNZ      B3      ; ERROR BIT SET?
1086 04E8 C3      RET
1087 04E9      INIT_DRV_R      ENDP

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1088
1089 ;----- SEND THE BYTE OUT TO THE CONTROLLER
1090
1091 04E9 INIT_DRV_S PROC NEAR
1092 04E9 E8 068D R CALL HD_WAIT ; GO WAIT FOR REQUEST
1093 04EC 72 05 JC D1 ; AFTER CALL DX = PORT 1
1094 04EE 4A DEC DX ; ADDRESS PORT 0
1095 04EF 26: 8A 01 MOV AL,ES:[BX+DI]
1096 04F2 EE OUT DX,AL ; WRITE THE DATA TO THE CARD
1097 04F3 D1: RET
1098 04F3 C3 INIT_DRV_S ENDP
1099 04F4
1100
1101 ;-----
1102 ; READ LONG (AH = 0AH) :
1103 ;-----
1104
1105 04F4 RD_LONG PROC NEAR
1106 04F4 E8 050E R CALL CHK_LONG ; CHECK LIMITS
1107 04F7 72 5F JC G8
1108 04F9 C6 46 F8 E5 MOV CMD_BLOCK+0,RD_LONG_CMD
1109 04FD B0 47 MOV AL,DMA_READ
1110 04FF EB 5D JMP SHORT DMA_OPN
1111 0501 RD_LONG ENDP
1112
1113 ;-----
1114 ; WRITE LONG (AH = 0BH) :
1115 ;-----
1116
1117 0501 WR_LONG PROC NEAR
1118 0501 E8 050E R CALL CHK_LONG ; CHECK LIMITS
1119 0504 72 52 JC G8
1120 0506 C6 46 F8 E6 MOV CMD_BLOCK+0,WR_LONG_CMD
1121 050A B0 4B MOV AL,DMA_WRITE
1122 050C EB 50 JMP SHORT DMA_OPN
1123 050E WR_LONG ENDP
1124
1125 050E CHK_LONG PROC NEAR
1126 050E 8A 46 FC MOV AL,CMD_BLOCK+4 ; LOAD THE NUMBER OF SECTORS
1127 0511 3C 80 CMP AL,080H ; COMPARE WITH LIMITS
1128 0513 F5 CMC ; SET THE CONDITION
1129 0514 C3 RET
1130 0515 CHK_LONG ENDP
1131
1132 ;-----
1133 ; SEEK (AH = 0CH) :
1134 ;-----
1135
1136 0515 DISK_SEEK PROC NEAR
1137 0515 C6 46 F8 0B MOV CMD_BLOCK+0,SEEK_CMD
1138 0519 EB 34 JMP SHORT NDMA_OPN
1139 051B DISK_SEEK ENDP
1140
1141 ;-----
1142 ; READ SECTOR BUFFER (AH = 0EH) :
1143 ;-----
1144
1145 051B RD_BUFF PROC NEAR
1146 051B C6 46 F8 0E MOV CMD_BLOCK+0,RD_BUFF_CMD
1147 051F C6 46 FC 01 MOV CMD_BLOCK+4,1 ; ONLY ONE BLOCK
1148 0523 B0 47 MOV AL,DMA_READ
1149 0525 EB 37 JMP SHORT DMA_OPN
1150 0527 RD_BUFF ENDP
1151
1152 ;-----
1153 ; WRITE SECTOR BUFFER (AH = 0FH) :
1154 ;-----
1155
1156 0527 WR_BUFF PROC NEAR
1157 0527 C6 46 F8 0F MOV CMD_BLOCK+0,WR_BUFF_CMD
1158 052B C6 46 FC 01 MOV CMD_BLOCK+4,1 ; ONLY ONE BLOCK
1159 052F B0 4B MOV AL,DMA_WRITE
1160 0531 EB 2B JMP SHORT DMA_OPN
1161 0533 WR_BUFF ENDP
1162
1163 ;-----
1164 ; TEST DISK READY (AH = 010H) :
1165 ;-----
1166
1167 0533 TST_RDY PROC NEAR
1168 0533 C6 46 F8 00 MOV CMD_BLOCK+0,TST_RDY_CMD
1169 0537 EB 16 JMP SHORT NDMA_OPN
1170 0539 TST_RDY ENDP
1171
1172 ;-----
1173 ; RECALIBRATE (AH = 011H) :
1174 ;-----
1175
1176 0539 HDISK_RECAL PROC NEAR
1177 0539 C6 46 F8 01 MOV CMD_BLOCK+0,RECAL_CMD
1178 053D EB 10 JMP SHORT NDMA_OPN
1179 053F HDISK_RECAL ENDP

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1180 PAGE
1181 ;-----
1182 ; CONTROLLER RAM DIAGNOSTICS (AH = 012H) :
1183 ;-----
1184
1185 053F RAM_DIAG PROC NEAR
1186 053F C6 46 F8 E0 MOV CMD_BLOCK+0, RAM_DIAG_CMD
1187 0543 EB 0A JMP SHORT NDMA_OPN
1188 0545 RAM_DIAG ENDP
1189
1190 ;-----
1191 ; DRIVE DIAGNOSTICS (AH = 013H) :
1192 ;-----
1193
1194 0545 CHK_DRV PROC NEAR
1195 0545 C6 46 F8 E3 MOV CMD_BLOCK+0, CHK_DRV_CMD
1196 0549 EB 04 JMP SHORT NDMA_OPN
1197 054B CHK_DRV ENDP
1198
1199 ;-----
1200 ; CONTROLLER INTERNAL DIAGNOSTICS (AH = 014H) :
1201 ;-----
1202
1203 054B CNTLR_DIAG PROC NEAR
1204 054B C6 46 F8 E4 MOV CMD_BLOCK+0, CNTLR_DIAG_CMD
1205 054F CNTLR_DIAG ENDP
1206
1207 ;-----
1208 ; SUPPORT ROUTINES :
1209 ;-----
1210
1211 054F NDMA_OPN:
1212 054F B0 02 MOV AL, 02H
1213 0551 E8 057C R CALL COMMAND ; ISSUE THE COMMAND
1214 0554 72 22 JC G11
1215 0556 EB 16 JMP SHORT G3
1216 0558
1217 0558 C6 06 0074 R 09 MOV DISK_STATUS, DMA_BOUNDARY
1218 055D C3 RET
1219 055E
1220 055E E8 06A5 R DMA_OPN: CALL DMA_SETUP ; SET UP FOR DMA OPERATION
1221 0561 72 F5 JC G8
1222 0563 B0 03 MOV AL, 03H
1223 0565 E8 057C R CALL COMMAND ; ISSUE THE COMMAND
1224 0568 72 0E JC G11
1225 056A B0 03 MOV AL, 03H
1226 056C E6 0A OUT DMA+10, AL ; INITIALIZE THE DISK CHANNEL
1227 056E
1228 056E FA G3: CLI ; NO INTERRUPTS
1229 056F E4 21 IN AL, INTA01 ; READ THE MASK
1230 0571 24 DF AND AL, 0DFH ; ENABLE IRQ-5
1231 0573 E6 21 OUT INTA01, AL ; WRITE THE MASK OUT
1232 0575 E8 0700 R CALL WAIT_INT ; PROCEDURE DOES STI
1233 0578
1234 0578 E8 05AD R G11: CALL ERROR_CHK ; SEE IF THERE IS AN ERROR
1235 057B C3 RET ; EXIT
1236
1237 ;-----
1238 ; COMMAND :
1239 ; THIS ROUTINE OUTPUTS THE COMMAND BLOCK :
1240 ; INPUT :
1241 ; AL = CONTROLLER DMA/INTERRUPT REGISTER MASK :
1242 ; :
1243 ;-----
1244
1245 057C COMMAND PROC NEAR
1246 057C E8 076D R CALL PORT_0 ; GET THE BASE ADDRESS
1247 057F 42 INC DX
1248 0580 42 INC DX ; ADDRESS PORT 2
1249 0581 EE OUT DX, AL ; ISSUE CONTROLLER SELECT PULSE
1250 0582 42 INC DX ; ADDRESS PORT
1251 0583 2B C9 SUB CX, CX ; WAIT COUNT
1252 0585 EE OUT DX, AL ; WRITE OMA MASK REGISTER
1253 0586 4A DEC DX
1254 0587 4A DEC DX ; ADDRESS PORT 1
1255 0588
1256 0588 EC WAIT_BUSY: IN AL, DX ; READ THE HARDWARE STATUS
1257 0589 24 0F AND AL, 0FH
1258 058B 3C 0D CMP AL, R1_BUSY OR R1_BUS OR R1_REQ ; CHECK FOR BUSY, COMMAND
1259 058D 74 09 JE C1 ; AND REQUEST BITS
1260 058F E2 F7 LOOP WAIT_BUSY ; KEEP TRYING
1261 0591 C6 06 0074 R 80 MOV DISK_STATUS, TIME_OUT
1262 0596 F9 STC ; SET THE ERROR CONDITION
1263 0597 C3 RET ; ERROR RETURN
1264 0598
1265 0598 B9 0006 C1: MOV CX, 6 ; SET FOR 6 BYTES OF COMMAND
1266 059B 4A DEC DX ; ADDRESS PORT 0
1267 059C 8B F5 MOV SI, BP ; SAVE THE BASE POINTER
1268 059E 83 ED 08 SUB BP, 8 ; SET FIRST BYTE OF COMMAND BLOCK
1269 05A1 FA CLI ; NO INTERRUPTS IN COMMAND SEQUENCE
1270 05A2
1271 05A2 8A 46 00 CM3: MOV AL, [BP] ; GET A COMMAND BYTE
1272 05A5 EE OUT DX, AL ; ALLOW AT LEAST 2us BETWEEN EACH BYTE
1273 05A6 45 INC BP ; ON SENDING THE COMMAND SEQUENCE
1274 05A7 E2 F9 LOOP CM3 ; DO MORE
1275 05A9 8B EE MOV BP, SI ; RESTORE THE BASE POINTER
1276 05AB FB STI ; INTERRUPTS BACK ON
1277 05AC C3 RET
1278 05AD COMMAND ENDP

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1279 PAGE
1280 ;-----
1281 ;
1282 ; SENSE STATUS BYTES
1283 ;
1284 ; BYTE 0
1285 ; BIT 7 ADDRESS VALID, WHEN SET
1286 ; BIT 6 SPARE, SET TO ZERO
1287 ; BITS 5-4 ERROR TYPE
1288 ; BITS 3-0 ERROR CODE
1289 ;
1290 ; BYTE 1
1291 ; BITS 7-6 ZERO
1292 ; BIT 5 DRIVE (0-1)
1293 ; BITS 4-0 HEAD NUMBER
1294 ;
1295 ; BYTE 2
1296 ; BITS 7-5 CYLINDER HIGH
1297 ; BITS 4-0 SECTOR NUMBER
1298 ;
1299 ; BYTE 3
1300 ; BITS 7-0 CYLINDER LOW
1301 ;-----
1302
1303 05AD ERROR_CHK PROC NEAR
1304 05AD A0 0074 R MOV AL,DISK_STATUS ; CHECK IF THERE WAS AN ERROR
1305 05B0 0A C0 OR AL,AL ; ANYTHING IN AL?
1306 05B2 75 01 JNZ G21
1307 05B4 C3 RET
1308
1309 ;-----
1310 ; PERFORM SENSE STATUS :
1311 ;-----
1312 ; SENSE STATUS CAN BE ISSUED MULTIPLE
1313 ; TIMES
1314 05B5
1315 05B5 C6 46 F8 03 G21: MOV CMD_BLOCK+0,SENSE_CMD
1316 05B9 2A C0 SUB AL,AL ; WRITE ZERO IN INT/DMA MASK
1317 05BB E8 057C R CALL COMMAND ; ISSUE SENSE STATUS COMMAND
1318 05BE 72 26 JC G24 ; CANNOT RECOVER-EXIT WITH COMMAND
1319 ; ERROR
1320 05C0 2B FF SUB DI,DI ; SET INDEX POINTER TO ZERO
1321 05C2 B9 0004 MOV CX,4 ; READ FOUR BYTES
1322 05C5 B4 0B MOV AH,00001011B ; SET MASK FOR DATA MODE CARD TO CPU
1323 05C7
1324 05C7 E8 068D R G22: CALL HD_WAIT ; GO WAIT FOR DATA INPUT STATE
1325 05CA 72 1A JC G24
1326 05CC 4A DEC DX ; ADDRESS PORT 0
1327 05CD EC IN AL,DX ; READ THE DATA BYTE 1
1328 05CE 88 43 F8 MOV [DI+CMD_BLOCK],AL ; STORE AWAY SENSE BYTES
1329 05D1 47 INC DI ; NEXT DATA LOCATION co
1330 05D2 E2 F3 LOOP G22 ; LOOP TILL ALL FOUR READ.
1331 05D4 B4 0F MOV AH,00001111B ; SET THE MASK FOR STATUS MODE
1332 05D6 E8 068D R CALL HD_WAIT ; GO WAIT FOR STATUS STATE
1333 05D9 72 0B JC G24
1334 05DB 4A DEC DX ; ADDRESS PORT 0
1335 05DC EC IN AL,DX ; READ THE STATUS BYTE
1336 05DD A8 02 TEST AL,2 ; SENSE OPERATION FAIL?
1337 05DF 74 0F JZ STAT_ERR ; GO GET THE ERROR.
1338
1339 05E1 C6 06 0074 R FF MOV DISK_STATUS,SENSE_FAIL ; SET SENSE OPERATION FAIL
1340 05E6
1341 05E6 F9 G24: STC
1342 05E7 C3 RET
1343 05E8
1344 ERROR_CHK ENDP
1345 05E8 061E R T_0 DW TYPE_0 ; ERROR TYPE JUMP TABLE
1346 05EA 062B R DW TYPE_1
1347 05EC 066D R DW TYPE_2
1348 05EE 067A R DW TYPE_3
1349
1350 05F0 STAT_ERR:
1351 05F0 8A 5E F8 MOV BL,CMD_BLOCK+0 ; GET ERROR BYTE
1352 05F3 8A C3 MOV AL,BL
1353 05F5 24 0F AND AL,0FH
1354 05F7 80 E3 30 AND BL,00110000B ; ISOLATE THE TYPE OF ERROR
1355 05FA 2A FF SUB BH,BH
1356 05FC B1 03 MOV CL,3
1357 05FE D3 EB SHR BX,CL ; ADJUST
1358 0600 2E: FF A7 05E8 R JMP WORD PTR CS:[BX + OFFSET T_0]
1359
1360 0605 TYPE0_TABLE LABEL BYTE
1361 0605 00 20 40 CC 80 00 DB 0,BAD_CNTLRL,BAD_SEEK,WRITE_FAULT,TIME_OUT,0,BAD_CNTLRL
1362 20
1363 060C 00 40 DB 0,BAD_SEEK
1364 = 0009 TYPE0_LEN EQU $-TYPE0_TABLE
1365
1366 060E TYPE1_TABLE LABEL BYTE
1367 060E 04 10 02 00 04 DB RECORD_NOT_FND,BAD_ECC,BAD_ADDR_MARK,0,RECORD_NOT_FND
1368 0613 40 00 00 11 0B DB BAD_SEEK,0,0,DATA_CORRECTED,BAD_TRACK
1369 = 000A TYPE1_LEN EQU $-TYPE1_TABLE
1370
1371 0618 TYPE2_TABLE LABEL BYTE
1372 0618 01 02 01 DB BAD_CMD,BAD_ADDR_MARK,BAD_CMD
1373 = 0003 TYPE2_LEN EQU $-TYPE2_TABLE
1374
1375 061B TYPE3_TABLE LABEL BYTE
1376 061B 20 20 10 DB BAD_CNTLRL,BAD_CNTLRL,BAD_ECC
1377 = 0003 TYPE3_LEN EQU $-TYPE3_TABLE

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1378                                     PAGE
1379 ;----- TYPE 0 ERROR
1380
1381 061E                                     TYPE_0:
1382 061E BB 0605 R                         MOV     BX,OFFSET TYPE0_TABLE
1383 0621 3C 09                             CMP     AL,TYPE0_LEN           ; CHECK IF ERROR IS DEFINED
1384 0623 73 62                             JAE     UNDEF_ERR_L
1385 0625 2E: D7                             XLAT    CS:TYPE0_TABLE         ; TABLE LOOKUP
1386 0627 A2 0074 R                         MOV     DISK_STATUS,AL       ; SET ERROR CODE
1387 062A C3                                 RET
1388
1389 ;----- TYPE 1 ERROR
1390
1391 062B                                     TYPE_1:
1392 062B BB 060E R                         MOV     BX,OFFSET TYPE1_TABLE
1393 062E 8B C8                             MOV     CX,AX
1394 0630 3C 0A                             CMP     AL,TYPE1_LEN           ; CHECK IF ERROR IS DEFINED
1395 0632 73 53                             JAE     UNDEF_ERR_L
1396 0634 2E: D7                             XLAT    CS:TYPE1_TABLE         ; TABLE LOOKUP
1397 0636 A2 0074 R                         MOV     DISK_STATUS,AL       ; SET ERROR CODE
1398 0639 80 E1 08                             AND     CL,08H                ; CORRECTED ECC
1399 063C 80 F9 08                             CMP     CL,08H
1400 063F 75 29                             JNZ     G30
1401
1402 ;----- OBTAIN ECC ERROR BURST LENGTH
1403
1404 0641 C6 46 F8 0D                         MOV     CMD_BLOCK+0,RD_ECC_CMD
1405 0645 2A C0                             SUB     AL,AL
1406 0647 E8 057C R                         CALL    COMMAND               ; ISSUE THE COMMAND
1407 064A 72 1E                             JC      G30
1408 064C B4 0B                             MOV     AH,00001011B          ; SET MASK FOR DATA INPUT CARD TO CPU
1409 064E E8 068D R                         CALL    HD_WAIT               ; GO WAIT FOR THE INPUT STATE
1410 0651 72 17                             JC      G30
1411 0653 4A                             DEC     DX                     ; ADDRESS PORT 0
1412 0654 EC                             IN      AL,DX                 ; READ THE LENGTH OF THE ERROR
1413 0655 8A C8                             MOV     CL,AL                 ; CORRECTED AND SAVE IN CL
1414 0657 B4 0F                             MOV     AH,00001111B          ; SET MASK FOR STATUS STATE
1415 0659 E8 068D R                         CALL    HD_WAIT               ; GO WAIT FOR STATUS STATE
1416 065C 72 0C                             JC      G30
1417 065E 4A                             DEC     DX                     ; ADDRESS PORT 0
1418 065F EC                             IN      AL,DX                 ; READ THE STATUS BYTE
1419 0660 A8 02                             TEST    AL,2                  ; ERROR BIT SET?
1420 0662 74 06                             JZ      G30
1421 0664 C6 06 0074 R 20                     MOV     DISK_STATUS,BAD_CNTL
1422 0669 F9                             STC
1423 066A
1424 066A 8A C1                             G30: MOV     AL,CL
1425 066C C3                                 RET
1426
1427 ;----- TYPE 2 ERROR
1428
1429 066D                                     TYPE_2:
1430 066D BB 0618 R                         MOV     BX,OFFSET TYPE2_TABLE
1431 0670 3C 03                             CMP     AL,TYPE2_LEN           ; CHECK IF ERROR IS DEFINED
1432 0672 73 13                             JAE     UNDEF_ERR_L
1433 0674 2E: D7                             XLAT    CS:TYPE2_TABLE         ; TABLE LOOKUP
1434 0676 A2 0074 R                         MOV     DISK_STATUS,AL       ; SET ERROR CODE
1435 0679 C3                                 RET
1436
1437 ;----- TYPE 3 ERROR
1438
1439 067A                                     TYPE_3:
1440 067A BB 061B R                         MOV     BX,OFFSET TYPE3_TABLE
1441 067D 3C 03                             CMP     AL,TYPE3_LEN           ; CHECK IF ERROR IS DEFINED
1442 067F 73 06                             JAE     UNDEF_ERR_L
1443 0681 2E: D7                             XLAT    CS:TYPE3_TABLE         ; TABLE LOOKUP
1444 0683 A2 0074 R                         MOV     DISK_STATUS,AL       ; SET ERROR CODE
1445 0686 C3                                 RET
1446
1447 0687                                     UNDEF_ERR_L:
1448 0687 C6 06 0074 R BB                     MOV     DISK_STATUS,UNDEF_ERR
1449 068C C3                                 RET
1450
1451 ;-----
1452 ; ON ENTRY AH CONTAINS THE CONTROLLER BUS STATUS DECODE :
1453 ; MASK USED TO CHECK THE HARDWARE STATUS. :
1454 ;-----
1455 068D                                     HD_WAIT PROC NEAR
1456 068D 51                                     PUSH    CX                    ; SAVE CX
1457 068E 2B C9                                     SUB     CX,CX                  ; SET THE LOOP COUNT
1458 0690
1459 0690 E8 076D R                         L1: CALL    PORT_0
1460 0693 42                                     INC     DX                     ; PORT_1 ADDRESS
1461 0694 EC                                     IN      AL,DX                 ; READ THE HARDWARE STATUS
1462 0695 24 0F                                     AND     AL,00001111B          ; CLEAR UPPER NIBBLE OF HARDWARE STATUS
1463 0697 3A C4                                     CMP     AL,AH                 ; CHECK THE STATE WITH THE MASK
1464 0699 74 08                                     JZ      L2                     ; JMP IF O.K WITH CARRY CLEARED
1465 069B E2 F3                                     LOOP    L1                     ; TRY AGAIN
1466 069D C6 06 0074 R 80                     MOV     DISK_STATUS,TIME_OUT
1467 06A2 F9                                     STC                             ; SET ERROR CONDITION
1468 06A3
1469 06A3 59                                     L2: POP     CX                  ; RESTORE CX
1470 06A4 C3                                     RET
1471 06A5                                     HD_WAIT ENDP

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1472                                     PAGE
1473                                     ;-----
1474                                     ; DMA_SETUP                                     :
1475                                     ; THIS ROUTINE SETS UP FOR DMA OPERATIONS.         :
1476                                     ; INPUT                                           :
1477                                     ; (AL) = MODE BYTE FOR THE DMA                     :
1478                                     ; (ES:BX) = ADDRESS TO READ/WRITE THE DATA       :
1479                                     ; OUTPUT                                          :
1480                                     ; (AX) DESTROYED                                  :
1481                                     ;-----
1482
1483 06A5                                DMA_SETUP    PROC    NEAR
1484 06A5 80 7E FC 81                    CMP        CMD_BLOCK+4,81H    ; BLOCK COUNT OUT OF RANGE
1485 06A9 72 02                        JB         J1
1486
1487 06AB F9                            STC                               ; SET THE ERROR CONDITION
1488 06AC C3                            RET
1489
1490 06AD                                J1:
1491 06AD FA                            CLI                               ; NO MORE INTERRUPTS
1492 06AE E6 0C                        OUT        DMA+12,AL             ; SET THE FIRST/LAST F/F
1493 06B0 B1 04                        MOV        CL,4                 ; SHIFT COUNT
1494 06B2 E6 0B                        OUT        DMA+11,AL          ; OUTPUT THE MODE BYTE
1495 06B4 8C C0                        MOV        AX,ES                 ; GET THE ES VALUE
1496 06B6 D3 C0                        ROL        AX,CL                 ; ROTATE LEFT
1497 06B8 8A E8                        MOV        CH,AL             ; GET HIGHEST NIBBLE OF ES TO CH
1498 06BA 24 F0                        AND        AL,0F0H          ; ZERO THE LOW NIBBLE FROM SEGMENT
1499 06BC 03 C3                        ADD        AX,BX             ; TEST FOR CARRY FROM ADDITION
1500 06BE 80 D5 00                    ADC        CH,0              ; CARRY MEANS HIGH 4 BITS MUST BE INC
1501
1502 06C1 8B F0                        MOV        SI,AX             ; SAVE START ADDRESS
1503 06C3 E6 06                        OUT        DMA+6,AL          ; OUTPUT LOW ADDRESS
1504 06C5 8A C4                        MOV        AL,AH            ;
1505 06C7 E6 06                        OUT        DMA+6,AL          ; OUTPUT HIGH ADDRESS
1506 06C9 8A C5                        MOV        AL,CH            ; GET HIGH 4 BITS
1507 06CB 24 0F                        AND        AL,0FH           ;
1508 06CD E6 82                        OUT        DMA_HIGH,AL      ; OUTPUT THE HIGH 4 BITS TO PAGE REG
1509
1510                                     ;----- DETERMINE COUNT
1511
1512 06CF 8A 66 FC                    MOV        AH,CMD_BLOCK+4    ; RECOVER BLOCK COUNT
1513 06D2 D0 E4                        SHL        AH,1             ; MULTIPLY BY 512 BYTES PER SECTOR
1514 06D4 32 C0                        XOR        AL,AL            ; CLEAR LOW BYTE
1515 06D6 48                            DEC        AX                ; AND DECREMENT VALUE BY ONE
1516
1517                                     ;----- HANDLE READ AND WRITE LONG (516D BYTE BLOCKS)
1518
1519 06D7 80 7E F8 E5                CMP        CMD_BLOCK+0,RD_LONG_CMD
1520 06DB 74 06                        JE         ADD4
1521
1522 06DD 80 7E F8 E6                CMP        CMD_BLOCK+0,WR_LONG_CMD
1523 06E1 75 0F                        JNE        J20
1524 06E3
1525 06E3 B8 0204                    ADD4:    MOV        AX,516D    ; ONE BLOCK (512) PLUS 4 BYTES ECC
1526 06E6 53                        PUSH     BX
1527 06E7 2A FF                        SUB      BH,BH
1528 06E9 8A 5E FC                    MOV      BL,CMD_BLOCK+4
1529 06EC 52                        PUSH     DX
1530 06ED F7 E3                        MUL      BX                ; BLOCK COUNT TIMES 516
1531 06EF 5A                        POP      DX
1532 06F0 5B                        POP      BX
1533 06F1 48                        DEC      AX                ; ADJUST
1534 06F2
1535 06F2 8B C8                        J20:    MOV      CX,AX      ; SAVE COUNT VALUE
1536 06F4 E6 07                        OUT      DMA+7,AL          ; LOW BYTE OF COUNT
1537 06F6 8A C4                        MOV      AL,AH            ;
1538 06F8 E6 07                        OUT      DMA+7,AL          ; HIGH BYTE OF COUNT
1539 06FA FB                        STI                               ; INTERRUPTS BACK ON
1540 06FB 8B C6                        MOV      AX,SI            ; RECOVER ADDRESS VALUE
1541 06FD 03 C1                        ADD      AX,CX            ; ADD, TEST FOR 64K OVERFLOW
1542 06FF C3                        RET                          ; RETURN TO CALLER
1543                                     ; CY SET BY ABOVE IF ERROR
1544 0700                                DMA_SETUP    ENDP

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1545 PAGE
1546 ;-----
1547 ; WAIT_INT :
1548 ; THIS ROUTINE WAITS FOR THE FIXED DISK :
1549 ; CONTROLLER TO SIGNAL THAT AN INTERRUPT :
1550 ; HAS OCCURRED. :
1551 ;-----
1552
1553 0700 WAIT_INT PROC NEAR
1554 ASSUME DS:ABS0
1555 0700 FB STI ; TURN ON INTERRUPTS
1556 0701 8C DB MOV EX,DS ; SAVE DS
1557 0703 2B C0 SUB AX,AX
1558 0705 8E D8 MOV DS,AX ; ESTABLISH SEGMENT
1559 0707 C4 36 0104 R LES SI,HF_TBL_VEC ; LOAD THE TABLE VECTOR
1560
1561 ASSUME DS:DATA,ES:NOTHING
1562 070B 8E DB MOV DS,BX ; RESTORE DS
1563
1564 ;----- SET TIMEOUT VALUES
1565
1566 070D 2A FF SUB BH,BH
1567 070F 26: 8A 5C 09 MOV BL,BYTE PTR ES:[SI][9] ; LOAD THE STANDARD TIME OUT
1568 0713 8A 66 F8 MOV AH,CMD_BLOCK+0
1569 0716 80 FC 04 CMP AH,FMTDRV_CMD
1570 0719 75 06 JNZ W5
1571
1572 071B 26: 8A 5C 0A MOV BL,BYTE PTR ES:[SI][0AH] ; LOAD THE FORMAT DRIVE
1573 071F EB 09 JMP SHORT W4 ; TIME OUT VALUE
1574 0721 80 FC E3 W5: CMP AH,CHK_DRV_CMD
1575 0724 75 04 JNZ W4
1576
1577 0726 26: 8A 5C 0B MOV BL,BYTE PTR ES:[SI][0BH] ; LOAD THE CHECK DRIVE
1578 072A W4: ; TIME OUT VALUE
1579 072A F8 CLC ; CLEAR CY
1580 072B B8 9000 MOV AX,9000H ; DEVICE WAIT INTERRUPT
1581 072E CD 15 INT 15H
1582 0730 FB STI ; ENABLE INTERRUPTS FOR PC AND
1583 ; XT MACHINES.
1584 0731 2B C9 SUB CX,CX ; SET THE LOOP COUNT
1585
1586 ;----- WAIT FOR INTERRUPT
1587
1588 0733 W1:
1589 0733 E8 076D R CALL PORT_0
1590 0736 42 INC DX ; PORT_1 ADDRESS
1591 0737 EC IN AL,DX ; READ THE HARDWARE STATUS
1592 0738 A8 20 TEST AL,020H ; DID INTERRUPT OCCUR
1593 073A 75 0A JNZ W2 ; JUMP IF YES
1594
1595 073C E2 F5 LOOP W1 ; INNER LOOP
1596 073E 4B DEC BX
1597 073F 75 F2 JNZ W1 ; OUTER LOOP
1598
1599 0741 C6 06 0074 R 80 MOV DISK_STATUS,TIME_OUT
1600 0746 W2:
1601 0746 4A DEC DX ; ADDRESS PORT 0
1602 0747 EC IN AL,DX ; READ THE STATUS BYTE
1603 0748 24 02 AND AL,2 ; ISOLATE THE ERROR BIT
1604 074A 08 06 0074 R OR DISK_STATUS,AL ; SAVE IN THE STATUS
1605 074E 83 C2 03 ADD DX,3 ; PORT_3 ADDRESS
1606 0751 32 C0 XOR AL,AL ; ZERO
1607 0753 EE OUT DX,AL ; RESET INTERRUPT MASK
1608 0754 C3 RET
1609
1610 0755 WAIT_INT ENDP
1611
1612 ;--- HD_INT -----
1613 ;
1614 ; FIXED DISK INTERRUPT 0DH ROUTINE IRQ-5 :
1615 ; :
1616 ;-----
1617
1618 0755 HD_INT PROC NEAR
1619 0755 50 PUSH AX ; SAVE WORK REGISTER
1620 0756 B0 07 MOV AL,07H ; SET DMA MODE TO DISABLE
1621 0758 E6 0A OUT DMA+10,AL
1622 075A FA CLI ; NO INTERRUPTS
1623 075B E4 21 IN AL,INTA01 ; LOAD THE INTERRUPT ENABLE MASK
1624 075D 0C 20 OR AL,020H ; TURN OFF FIXED DISK IRQ-5
1625 075F E6 21 OUT INTA01,AL ; REPLACE THE MASK
1626 0761 B0 20 MOV AL,EOI ; LOAD THE END OF INTERRUPT MASK
1627 0763 E6 20 OUT INTA00,AL ; CLEAR THE ACTIVE INTERRUPT LEVEL
1628 0765 FB STI ; INTERRUPTS BACK ON
1629 0766 B8 9100 MOV AX,9100H ; DEVICE POST
1630 0769 CD 15 INT 15H ; INTERRUPT
1631 076B 58 POP AX ; RESTORE AX
1632 076C CF IRET
1633 076D HD_INT ENDP
1634
1635 ;-----
1636 ; PORTS :
1637 ; GENERATE PROPER PORT VALUE :
1638 ; BASED ON THE PORT OFFSET :
1639 ;-----
1640
1641 076D PORT_0 PROC NEAR
1642 076D BA 0320 MOV DX,HF_PORT ; BASE VALUE
1643 0770 02 16 0077 R ADD DL,PORT_OFF ; ADD IN OFFSET VALUE (00,04,08,0C)
1644 0774 C3 RET
1645 0775 PORT_0 ENDP
1646
1647 0775 END_ADDRESS LABEL BYTE
1648 0775 CODE ENDS
1649 END

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